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(54) Method and apparatus for optimizing the execution of software applications

Verfahren und Gerät zur Optimierung des Programmablaufs von Anwendungen

Méthode et appareil pour optimiser l'exécution d'applications

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Description**BACKGROUND OF THE INVENTION****1. Field of Invention**

[0001] The present invention relates generally to methods and apparatus for optimizing the execution of software applications. More particularly, the present invention relates to methods and apparatus for performing compilations or other activities during pauses in the overall execution of a computer program to optimize the use of computer system resources.

2. Description of Relevant Art

[0002] Computer systems are often linked across a network, *e.g.*, local area networks, intranets and internets, of computer systems such that they may share resources such as software applications. In general, software applications, or computer programs, may be delivered in different formats to different computer systems, due to the fact that each computer system requires software applications to be in a specific format. Alternatively, the computer programs may be delivered to a computer system in a machine-independent form, *i.e.*, as byte codes, in order to enable one form of a computer program to be utilized by many different computer systems.

[0003] When computer programs are delivered in a machine-independent form, the programs may be interpreted directly, or the programs may be translated into machine-dependent code, *i.e.*, "machine code." Programs which are interpreted directly occupy less space in a computer system than programs which are translated into machine code. However, programs which are interpreted directly have slower execution speeds than programs which are translated into machine code, in most cases. As such, the determination of whether or not to interpret a computer program directly, in lieu of translating the computer program into machine code, is often based on the relative importance of space in relation to execution speed.

[0004] One approach to compilation is to compile methods within a program that is actively executing when they are first called for execution. This approach is frequently referred to as either dynamic or "runtime" compilation. One problem with dynamic compilation is that the compilation overhead associated with the program may become excessive. That is, when a relatively large number of methods must be compiled at about the same time, the overhead associated with the compilation of the program may be at a level that adversely affects the overall execution of the program. In general, an overuse of system resources for compilation may lead to a less efficient, slower execution of the program. In addition, this time consumed by a compiler which is compiling methods during active program execution may introduce user-visible pauses into the execution of the program. By way of

example, when a user inputs a command into the program while the compilation overhead of the program is high, there may be a delay in the response to the user input. Such delays in response may be annoying to a user. Therefore, mechanisms that improve the apparent efficiency of dynamic compilation of methods in a computer program would be desirable.

[0005] FRANZ M: "Run-Time Code Generation as a Central System Service" WORKSHOP ON HOT TOPICS IN OPERATING SYSTEMS, 5 May 1997, pages 112 to 117 discloses methods for continuously re-compiling compiled code during execution of a computer program, during the idle periods of the processing of a computer program, in order to improve the optimisation of compiled code. The continuous re-compilation of compiled code also allows dynamically linked code to have the appearance and advantages of a monolithic application while still being extensible.

[0006] To achieve the foregoing and other objects of the invention, methods and apparatus for dynamically compiling methods during the idle periods in the execution of a computer program are disclosed. The described methods are particularly suitable for use in computer systems that are arranged to execute both interpreted and compiled byte codes. According to one aspect of the present invention there is provided a computer-implemented method for dynamically compiling methods during an idle period in the processing of a computer program, the computer-implemented method comprising: identifying the idle period; identifying a first interpretable method selected from a plurality of interpretable methods during the idle period, the plurality of methods being included in the computer program, wherein the computer program is executable using both interpreted program code and compiled program code; and initializing a compilation of the first method, wherein the initialization of the compilation of the first method occurs during the idle period.

[0007] In one embodiment, if an interrupt is received during the compilation of a method, the compilation is allowed to continue for a predetermined period of time. If the compilation is not completed during the predetermined period of time, then the compilation is aborted.

[0008] The method as can further including completing the compilation of the first method when an interrupt is received.

[0009] According to a further aspect of the invention, there is provided a computer system for dynamically compiling methods during an idle period in the processing of a computer program, the computer system comprising: a mechanism for identifying the idle period; a mechanism for identifying a first interpretable method selected from a plurality of interpretable methods during the idle period, the plurality of methods being included in the computer program and the computer program being executable using both interpreted program code and compiled program code; and a compiler for initializing a compilation of the first method during the idle period.

[0010] According to a further aspect, there is provided a computer program code executable by a data processing device to provide the method and computer system aspects of the invention. There is also provided a computer program product comprising a computer readable medium storing the computer program code according to that aspect of the invention.

[0011] These and other advantages of the present invention will become apparent upon reading the following detailed descriptions and studying the various figures of the drawings.

[0012] The invention may best be understood by reference to the following description taking in conjunction with the accompanying drawings in which:

Figure 1 is a block diagram representation of a computer system which dynamically compiles code in accordance with an embodiment of the present invention.

Figure 2 is a process flow diagram which illustrates the steps associated with executing a program which includes the capability of compiling code during pauses in accordance with an embodiment of the present invention.

Figure 3 is a process flow diagram which illustrates the steps associated with performing compilations on methods, *i.e.*, step 216 of Figure 2, in accordance with an embodiment of the present invention.

Figure 4 is a process flow diagram which illustrates the steps associated with identifying the highest priority method, *i.e.*, step 302 of Figure 3, in accordance with an embodiment of the present invention.

Figure 5 illustrates a typical, general purpose computer system suitable for implementing the present invention.

Figure 6 is a diagrammatic representation of a virtual machine which is suitable for implementing the present invention.

DETAILED DESCRIPTION Of THE EMBODIMENTS

[0013] As described above, the dynamic compilation of methods associated with a byte-coded program while the program is actively executing may cause the program to execute inefficiently. This inefficiency may be a result of an excessive use of available computer system resources by the compiler, or compilation overhead. The time consumed by a compiler that is dynamically compiling methods during active program execution may introduce delays into the execution of the program. When such delays are perceptible to a user, the delays may be considered to be unacceptable, especially when the length of a delay is relatively long, *e.g.*, more than approximately 200 milliseconds.

[0014] One approach to improving the performance of such programs is to permit the mixing of both interpreted and dynamically compiled byte codes. Within such a system, decisions must still be made as to when it is efficient

to compile a particular method. In the present invention, method and an apparatus are described for delaying the dynamic compilation of methods in a computer program when compilation overhead is relatively high. More specifically, when it is determined that compilation overhead is too high, the dynamic compilation of at least some of the methods may be delayed until there are periods of relatively lower activity in the overall processing of the program.

[0015] By delaying the dynamic compilation of at least some byte-coded methods which are slated for compilation, computer resources may be more efficiently utilized. While a computer program is essentially inactive during processing, *e.g.*, when the computer program is idle while awaiting input from a user, the overall resource utilization overhead associated with the execution of the program is generally relatively low. During such periods of low activity, available system resources may be used to perform compilations of methods which have previously been identified as being methods which are to be compiled. Performing compilations while the computation overhead of the computer program is low allows for the efficient usage of overall system resources and, typically, does not cause delays which are noticeable to a user.

[0016] With initial reference to Figure 1, a computer system which allows byte codes to be dynamically compiled will be described in accordance with an embodiment of the present invention. Byte codes 144 are provided as input to a computer system 146 at run-time. Byte codes 144, which may generally be organized as a computer program, are typically arranged in parseable segments such as methods, or routines. In one embodiment, byte codes 144 may be provided by a compile-time environment within a virtual machine to a run-time environment, *e.g.*, computer system 146, within the same virtual machine. One suitable virtual machine on which the present invention may be implemented will be discussed below in more detail with respect to Figure 6.

[0017] When byte codes 144 are provided to computer system 146, byte codes 144 may be processed with an interpreter 148. Alternatively, byte codes 144 may be compiled by a compiler 150 to produce compiled byte codes 152. It should be appreciated that byte codes 144 may generally be inputted to both interpreter 148 and compiler 150 for processing, substantially simultaneously.

[0018] Each time a method is invoked, if the method is not compiled, byte codes 144 associated with the method are interpreted using interpreter 148. In one embodiment, a measure of how many times a method is interpreted is maintained. Such measures may include a counter, *e.g.*, an invocation counter, which is included in each interpreted method, and is incremented each time the method is interpreted.

[0019] When the number of times a method is interpreted exceeds a threshold, *i.e.*, a limiting value, the method may be compiled using compiler 150. Repeatedly interpreting a method, which is included in frequently

executed code 158, may be inefficient, as interpreted byte code 154 generally executes slower, or less efficiently, than compiled code. Compiling frequently executed code 158 generally may allow methods embodied in frequently executed code 158 to be executed more efficiently, as time-savings gained by compiling the method is likely to compensate for the compilation overhead associated with the compilation process.

[0020] During run-time, the compilation overhead associated with compiling byte codes 144 using compiler 150 is monitored to ensure that the compilation overhead does not exceed a typically predetermined maximum acceptable level. The compilation overhead is often expressed in terms of the percentage of processor time required for compilation as opposed to execution. The predetermined maximum overhead level may be widely varied depending on the needs and characteristics of a particular system. By way of example, for a virtual machine intended to provide high execution performance, the predetermined maximum level may be within the range of approximately 10 percent to approximately 30 percent usage of the overall system resources. When the overhead associated with the compilation of byte codes 144 exceeds the predetermined maximum level, then methods which would be compiled if the compilation overhead were lower may be placed in an execution list, or queue. This execution list is essentially a waiting list of methods which may be processed when the compilation overhead is lower, *i.e.*, methods in the execution list may be compiled when the compilation overhead is lower. Specifically, the execution list may be processed at times when both the compilation overhead, as well as the overall computation overhead associated with computer system 146, are at a low level.

[0021] When byte codes are to be compiled during otherwise idle periods in the overall execution of a program, *e.g.*, during idle periods which occur throughout the processing of byte codes, the program may be monitored to determine when pauses occur. Referring next to Figure 2, a process of executing a program, or an application, which includes the capability of compiling code during pauses will be described in accordance with an embodiment of the present invention. The execution of a program begins at step 210, and in step 212, a determination is made regarding whether the execution of the program has reached an idle period.

[0022] An idle period is generally a pause, *e.g.*, a think pause, in the processing of the program during which the program is essentially inactive. The program may be awaiting an action such as a timer signal from the associated operating system, or the program may be awaiting a user input, prior to actively continuing execution. In general, a think pause occurs while the program is awaiting an action from a user. It should be appreciated that the length of an idle period may vary widely. By way of example, the length of an idle period may vary from very few milliseconds to approximately half a second. When an idle period occurs because a program is awaiting input

from a user, the idle period may generally be much longer, as for example on the order of a number of seconds, minutes, days or more.

[0023] In general, a threshold value is used to recognize a "useful" pause. For example, if the actual pause has lasted for a predetermined number of milliseconds, then the system may recognize the pause as a useful pause which is likely to continue for a whole longer. When the pause is likely to continue, then compilations may be allowed to continue in order to exploit the pause. As such, the determination in step 212 regarding whether the execution of the program has reached an idle period involves determining whether a period of low activity has reached a threshold value, *e.g.*, approximately 100 milliseconds or approximately half a second.

[0024] In general, the occurrence of an idle period, or period of relatively low activity, may be identified by monitoring the usage of the central processing unit (CPU) associated with the execution of the program. Alternatively, the existence of an idle period may be determined by monitoring the status of threads associated with the overall computer system.

[0025] When CPU usage is monitored to identify periods of relative inactivity, CPU usage is essentially monitored to determine when the CPU usage falls below a certain usage threshold. The usage threshold, *i.e.*, a "low activity threshold," may be widely varied. By way of example, the usage threshold may be set such that when substantially only activities with low overhead, such as setting a cursor to periodically blink, are running, the program is considered to be idle. In one embodiment, when CPU usage during the processing of the program falls below a usage threshold which is approximately 20 percent of the overall system resources, then the overall processing of the program is considered to be in a period of low activity. At such times, at least approximately 80 percent of the system resources may be available for use.

[0026] Monitoring the status of threads may involve studying a thread scheduler, as will be appreciated by those skilled in the art. When the thread scheduler indicates that all threads are blocked, *i.e.*, substantially none of the threads are in a "run-able" state, then the implication is that the program is not consuming CPU time. CPU time will generally not be consumed until a signal arrives, at which point a thread may become unblocked. When CPU time is not being consumed, then the program is generally in an idle period.

[0027] If it is determined in step 212 that the execution of the program is not in an idle period, then process flow returns to step 210 where the program continues to execute. If, instead, it is determined in step 212 that there is an idle period, a determination is made in step 214 regarding whether there are any pending tasks, as for example compilations. That is, a determination is made regarding whether there are any tasks which are slated to occur during idle periods. The tasks may generally include a wide variety of tasks, as for example compilations and garbage collection. Such tasks may be priori-

tized in terms of their relative importance within a system. In the described embodiment, the tasks will be described in terms of pending compilations.

[0028] Pending compilations may generally be obtained from two sources, a database list and an execution list, which are lists of "candidate" methods for compilation. One arrangement for creating a database list may involve a system which monitors compiled methods, such as an arrangement for creating a database list when a database is inlined. In such a system, the database list is essentially a "working set," or a "candidate set," of methods which were compiled during a previous execution of the program, and are considered as methods that are likely to be compiled in a subsequent execution of the program. Other mechanisms may be used to create or add to a database list as well. It should be appreciated that in some embodiments, the database list may include other tasks such as the performance of garbage collection.

[0029] An execution list, as mentioned above, is a queue of methods, or a candidate set of methods, which is created during the current execution of the program. The execution list may contain, or identify, methods which were suppressed from compilation due to the fact that the compilation overhead was previously considered to be too high to allow the methods to be compiled. The execution list may also identify methods which were periodically added to the list by a separate sweeper process which studies the invocation counters, *i.e.*, counters which track the number of times a method has been interpreted, associated with interpreted methods. In general, the invocation counter for a method is incremented each time the method is invoked. The sweeper may periodically study invocation counters to determine if invocation counters of methods which have not been recently invoked are at a level which is currently considered to deem the method as appropriate for compilation. Since the threshold, *e.g.*, the number of invocations of a method which must generally be reached in order for the method to be considered for compilation, may vary during program execution, methods with invocation counters that are over a current threshold may be added to the execution list by the sweeper. Other mechanisms may be used to create or add to the execution list as well. In some embodiments, the execution list may be associated with a general work list which includes pending compilations in addition to other pending tasks, *e.g.*, garbage collection.

[0030] When it is determined through studying the execution list and the database list in step 214 that there are no pending tasks, then process flow returns to step 210 in which the program continues to execute. When, on the other hand, it is determined that there are pending compilations, then in step 216, the tasks, *e.g.*, compilations, are performed. The steps associated with performing the pending compilations will be described below with reference to Figure 3. After pending compilations are performed, process flow returns to step 2.10 in which the

program continues to execute.

[0031] As previously mentioned, in the described embodiment, a method is compiled when it is anticipated that compiling the method and invoking the compiled method is likely to be more efficient than interpreting the method. Such compilations may be delayed until there are idle periods during the execution of the overall program, in order to avoid excessive compilation overhead. Figure 3 is a process flow diagram which illustrates the 5 steps associated with performing delayed compilations on methods, *i.e.*, step 216 of Figure 2, in accordance with an embodiment of the present invention. The process of performing compilations begins at step 302 where the candidate method with the highest priority is identified. 10 In one embodiment, the highest priority method may be the method with the highest number of invocation counts in, or identified by, the execution list. Alternatively, the highest priority method may be the method with the shortest anticipated compilation time on the execution list. The 15 identification of the highest priority method will be discussed in more detail below with reference to Figure 4.

[0032] After the highest priority method is identified in step 302, a determination is made in step 304 regarding whether the highest priority method has already been 20 compiled. The highest priority method may already be compiled for a variety of different reasons. By way of example, after the method is placed in the execution list, during the course of overall program execution, the method may be invoked and compiled while compilation overhead is low. As such, the method is already compiled, and may not have been removed from the execution list, as repeatedly updating the execution list is expensive. The highest priority method may also be previously compiled if the method is obtained from the database list. 25

That is, when the same method appears in both the execution list and the database list, if the method was already compiled using the execution list, for example, and the database list is not updated due to high costs, then the method may already be compiled when it is identified 30 as the highest priority method in the database list.

[0033] When the determination in step 304 is that the highest priority method is already compiled, then the method may be removed from the appropriate list, *i.e.*, either the execution list or the database list, and process 35 flow returns to step 302 where a new highest priority method is identified. If, instead, it is determined that the highest priority method has not been previously compiled, then in step 306, the compilation of the highest priority method begins. That is, the compilation of the highest priority method is initialized.

[0034] During the compilation of the highest priority method, interrupts may be received. In general, interrupts may include, but are not limited to, timer signals and user inputs, *e.g.*, keyboard inputs. It is determined in step 308 55 whether an interrupt has been received. As will be appreciated by those skilled in the art, when an interrupt is received, the compilation of the highest priority method may automatically cease in order for the interrupt to be

processed as necessary.

[0035] If an interrupt has been received, then the compilation of the highest priority method is allowed to continue for a predetermined period of time in step 314. The predetermined period of time is, in one embodiment, essentially a "time to live" for a compilation after a signal has been received that will set at least one thread in a run-able state. By allowing the compilation to continue briefly after an interrupt is received, the compilation is given a chance to be completed, in the event that the compilation is close to completion.

[0036] Although the predetermined period of time may be widely varied, the predetermined period of time is generally a number of milliseconds, e.g., "X milliseconds." Shorter periods of time in the range of approximately 10 milliseconds to approximately 200 milliseconds are preferred, since it has been observed that delays of time of greater than approximately 200 milliseconds in response to user input are perceptible and, hence, often annoying to a user. The period of time may be varied based on the source of the signal or the priority of the threads in a run-able state, with shorter periods being used for higher-priority signals or threads.

[0037] After the compilation is allowed to continue for the predetermined period of time in step 314, a determination is made in step 316 regarding whether the compilation has been successfully completed during the predetermined period of time. If the compilation has been completed, then process flow moves to step 210 of Figure 2, where the overall execution of the program continues. It should be appreciated that once the compilation is completed, the newly compiled method will also be removed from either the execution list or the database list, depending upon which list the method was obtained from.

[0038] When the compilation is not completed during the predetermined period of time, then process flow moves from step 316 to step 318 in which the compilation is aborted. The compilation is not allowed to be completed due to the fact that allowing the compilation to be completed may result in a significant delay in the execution of the overall program or, more specifically, the processing of the interrupt. When the compilation is aborted, "clean up" occurs, e.g., system resources used in the attempted compilation are relinquished, as will be understood by those skilled in the art. Once the compilation is aborted, process flow moves to step 210 of Figure 2, in which the overall execution of the program continues.

[0039] Returning to step 308, in the event that no interrupt has been received during the compilation of the highest priority method, the compilation continues in step 310. A determination is made in step 312 regarding whether the compilation has been completed. If it is determined that the compilation has been completed, the highest priority method may be removed from the appropriate list, and process flow returns to step 302 in which a new highest priority method is identified. Alternatively, if it is determined in step 312 that the compilation has not

been completed, then the compilation is allowed to continue until either it is completed, or it is aborted due to the receipt of an interrupt in step 308.

[0040] In general, the "order" in which methods in an execution list and a database list are compiled may be determined based upon a number of different factors. The methods in each list may be assigned priority values relative to other methods in the same list. The priority values may generally be assigned at any time during the overall execution of the program, and may further be updated throughout the course of program execution. By way of example, the priority values in a list may be computed and updated, i.e., re-evaluated, each time a method is added to the list. Priority values may also be re-evaluated at the beginning of each idle period to ensure that the priorities of methods in the execution list and the database list are current at the time methods are chosen for compilation.

[0041] The priority value of a method may be calculated based on any combination of factors including, but not limited to, the invocation count of the method, the position of the method within the list, the estimated compilation time of the method, and the length of the current idle period during the overall execution of the program.

25 Alternatively, in one embodiment, the priority value of a method may essentially be assigned randomly.

[0042] The method which has the highest invocation count in a list may have the highest compilation priority, as it is essentially the most frequently invoked interpreted method. The position of a method within a list may also affect the priority value assigned to the method, since the method most recently added to the list is often likely to be needed in the immediate future. The method which has the shortest estimated compilation time in a list may have the highest compilation priority, since the compilation of a method with a shorter estimated compilation time is generally more likely to be completed during an idle period than the compilation of a method with a longer estimated compilation time. In general, the compilation time of a method may be estimated by the length of the method, e.g., a relatively short method will typically have a relatively short compilation time.

[0043] The length of the current idle period may also affect the priority value of a method. For example, if the current idle period has already been relatively long, then the likelihood that the idle period will last longer is generally increased. Long idle periods may often be a result of a user leaving the computer on which the overall execution of the program is occurring for a prolonged period of time. As such, it has been observed that when it is determined that a current idle period has already been relatively long, the likelihood that the idle period will last even longer is generally reasonably high. Therefore, when an idle period is expected to be relatively long,

55 methods with longer expected compilation times may have higher compilation priorities, since methods with shorter expected compilation times may typically be readily compiled during subsequent shorter idle periods.

[0044] As previously mentioned, the priority value of a method may be based on a number of different factors. In other words, a priority function may be used to determine a priority value. The form of such a priority function may generally be widely varied. By way of example, a priority function may divide the invocation count for a method by the estimated compilation time for the method in order to obtain a priority value for the method. Alternatively, a priority function may involve using the invocation count for methods with shorter estimated compilation times and using the invocation count divided by a constant for methods with longer estimated compilation times.

[0045] Referring next to Figure 4, the steps associated with identifying the method with the highest priority value, *i.e.*, step 302 of Figure 3, will be described in accordance with an embodiment of the present invention. The identification of the method with the highest priority begins at step 402 with a determination of whether any methods are present in the execution list associated with the program.

[0046] In general, methods in an execution list are considered as having compilation priorities that are higher than those of methods in a database list. Methods in the execution list are methods that have been identified during the course of program execution as being likely to overcome the associated compilation overhead by executing more efficiently, because the methods are repeatedly invoked. Alternatively, methods in the database list are methods which may potentially be repeatedly invoked and, hence, compiling the methods is likely to be beneficial. In other words, the execution list reflects the current, or actual, usage of methods, while the database list essentially reflects long-term usage of methods. Therefore, methods from the execution list are typically chosen for compilation before methods from the database list, since compiling the methods in the execution list is more likely to lead to a more efficient overall execution of the program which uses the methods.

[0047] If the determination in step 402 is that there are methods in the execution list, then in step 404, the method with the highest priority in the execution list is selected for compilation. Once the method is selected from the execution list, then the process of identifying the highest priority method is completed. Alternatively, if the determination in step 402 is that there are no methods in the execution list, then the highest priority method in the database list is selected for compilation. After the method is selected from the database list, then the process of identifying the highest priority method is completed.

[0048] The present invention may generally be implemented on any suitable computer system. Specifically, the compilation of queued methods during idle periods in the overall execution of a program may be accomplished using any suitable virtual machine, such as the virtual machine described below with respect to Figure 6. Figure 5 illustrates a typical, general purpose computer system suitable for implementing the present invention.

The computer system 530 includes any number of processors 532 (also referred to as central processing units, or CPUs) that are coupled to memory devices including primary storage devices 534 (typically a read only memory, or ROM) and primary storage devices 536 (typically a random access memory, or RAM).

[0049] Computer system 530 or, more specifically, CPU 532, may be arranged to support a virtual machine, as will be appreciated by those skilled in the art. One example of a virtual machine that is supported on computer system 530 will be described below with reference to Figure 6. As is well known in the art, ROM acts to transfer data and instructions uni-directionally to the CPU 532, while RAM is used typically to transfer data and instructions in a bi-directional manner. CPU 532 may generally include any number of processors. Both primary storage devices 534, 536 may include any suitable computer-readable media. A secondary storage medium 538, which is typically a mass memory device, is also coupled bi-directionally to CPU 532 and provides additional data storage capacity. The mass memory device 538 is a computer-readable medium that may be used to store programs including computer code, data, and the like. Typically, mass memory device 538 is a storage medium such as a hard disk or a tape which is generally slower than primary storage devices 534, 536. Mass memory storage device 938 may take the form of a magnetic or paper tape reader or some other well-known device. It will be appreciated that the information retained within the mass memory device 538, may, in appropriate cases, be incorporated in standard fashion as part of RAM 534 as virtual memory. A specific primary storage device 536 such as a CD-ROM may also pass data unidirectionally to the CPU 532.

[0050] CPU 532 is also coupled to one or more input/output devices 540 that may include, but are not limited to, devices such as video monitors, track balls, mice, keyboards, microphones, touch-sensitive displays, transducer card readers, magnetic or paper tape readers, tablets, styluses, voice or handwriting recognizers, or other well-known input devices such as, of course, other computers. Finally, CPU 532 optionally may be coupled to a computer or telecommunications network, *e.g.*, a local area network, an internet network or an intranet network, using a network connection as shown generally at 512. With such a network connection, it is contemplated that the CPU 532 might receive information from the network, or might output information to the network in the course of performing the above-described method steps. Such information, which is often represented as a sequence of instructions to be executed using CPU 532, may be received from and outputted to the network, for example, in the form of a computer data signal embodied in a carrier wave. The above-described devices and materials will be familiar to those of skill in the computer hardware and software arts.

[0051] As previously mentioned, a virtual machine may execute on computer system 530. Figure 6 is a diagram-

matic representation of a virtual machine which is supported by computer system 530 of Figure 5, and is suitable for implementing the present invention. When a computer program, *e.g.*, a computer program written in the Java™ programming language developed by Sun Microsystems of Mountain View, California, is executed, source code 610 is provided to a compiler 620 within a compile-time environment 605. Compiler 620 translates source code 610 into byte codes 630. In general, source code 610 is translated into byte codes 630 at the time source code 610 is created by a software developer.

[0052] Byte codes 630 may generally be reproduced, downloaded, or otherwise distributed through a network, *e.g.*, network 512 of Figure 5, or stored on a storage device such as primary storage 534 of Figure 5. In the described embodiment, byte codes 630 are platform independent. That is, byte codes 630 may be executed on substantially any computer system that is running a suitable virtual machine 640. By way of example, in a Java™ environment, byte codes 630 may be executed on a computer system that is running a Java™ virtual machine.

[0053] Byte codes 630 are provided to a runtime environment 635 which includes virtual machine 640. Runtime environment 635 may generally be executed using a processor such as CPU 532 of Figure 5. Virtual machine 640 includes a compiler 642, an interpreter 644, and a runtime system 646. Byte codes 630 may generally be provided either to compiler 642 or interpreter 644.

[0054] When byte codes 630 are provided to compiler 642, methods contained in byte codes 630 are compiled into machine instructions, as described above. On the other hand, when byte codes 630 are provided to interpreter 644, byte codes 630 are read into interpreter 644 one byte code at a time. Interpreter 644 then performs the operation defined by each byte code as each byte code is read into interpreter 644. In general, interpreter 644 processes byte codes 630 and performs operations associated with byte codes 630 substantially continuously.

[0055] When a method is called from an operating system 660, if it is determined that the method is to be invoked as an interpreted method, runtime system 646 may obtain the method from interpreter 644. If, on the other hand, it is determined that the method is to be invoked as a compiled method, runtime system 646 activates compiler 642. Compiler 642 then generates machine instructions from byte codes 630, and executes the machine-language instructions. In general, the machine-language instructions are discarded when virtual machine 640 terminates.

[0056] A highest priority method for compilation has been described as being selected out of substantially all methods in an execution list and a database list. Searching all methods in an execution list or a database list, or both, to locate the method with the highest compilation priority may prove to be expensive in the event that the execution list is extensive. Therefore, in one embodiment, a subset of the execution list may be searched, in

lieu of substantially the entire execution list, to identify a highest priority method for compilation may. By way of example, the first "N" candidate methods from the execution list may be searched to identify the highest priority method in the first N candidate methods.

[0057] As previously mentioned, a compilation priorities of methods may be identified, at least in part, using the invocation counters. The invocation counters are generally counters which are incremented each time a method is accessed. It should be appreciated that in some embodiments, invocation counters may be decayed over time. For example, if a method was repeatedly invoked and slated for compilation at the beginning of program execution, but is never again invoked, the invocation counter may be decayed to reduce the compilation priority of the method. Once the invocation counter for a method has been decayed, *e.g.*, exponentially decayed, compiling the method may no longer be advisable. As such, such methods may be periodically removed from the execution list.

[0058] Similarly, during the course of performing compilations, a determination is made regarding whether a method which is slated for compilation has previously been compiled. Alternatively, methods which have already been compiled may be removed as appropriate from either or both the execution list and the database list. In general, the overhead associated with checking the execution list and the database list for compiled methods may be relatively significant. However, compiled methods may be periodically removed, as for example at the beginning of an idle period.

[0059] Although an execution list has generally been described as being a list, or queue, of interpreted methods which are to be compiled, the execution list may also include compiled methods which are delayed from being re-compiled until there is an idle period. For example, in a system with two levels of compilers, once a method is compiled, if that method is repeatedly executed, it may prove to be desirable for the method to be re-compiled more efficiently. In such a system, the methods slated for re-compilations, or secondary compilations, may be included in the same execution list as interpreted methods which are slated for compilation. However, the methods slated for re-compilations may, on the other hand, be included in a separate execution list.

[0060] In one embodiment, the execution list may generally be a work list which includes delayed tasks, in addition to, or in lieu of, pending compilations. Such tasks may be prioritized. It should be appreciated that when a task from the work list is running, if an interrupt is received, the task may be allowed to be completed. By way of example, if the task involves garbage collection, suspending a garbage collection process and "backing out" of the garbage collection process may be as costly, if not more costly, than completing the process. As such, the garbage collection may be allowed to be completed. Tasks may also generally be suspended when an interrupt is received. Therefore, the present examples are to

be considered as illustrative and not restrictive, and the invention is not to be limited to the details given herein, but may be modified within the scope of the appended claims along with their full scope of equivalents.

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Claims

1. A computer-implemented method for dynamically compiling methods during an idle period in the processing of a computer program, the computer-implemented method comprising:

identifying (212) the idle period;
 identifying (302) a first interpretable method selected from a plurality of interpretable methods during the idle period, the plurality of methods being included in the computer program, wherein in the computer program (144) is executable using both interpreted program code (154) and compiled program code (152); and
 initializing (306) a compilation of the first method, wherein the initialization of the compilation of the first method occurs during the idle period.

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2. A computer-implemented method as recited in claim 1 wherein the plurality of methods is referenced in a first list associated with the computer program and the first method is a highest priority method associated with the first list.

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3. A computer-implemented method as recited in claim 1 wherein a first set of methods selected from the plurality of methods is associated with a first list and a second set of methods selected from the plurality of methods is associated with a second list, the first list being created during the processing of the computer program the second list being created before the processing of the computer program, wherein identifying the first method includes:

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selecting the first method as a highest priority method from the first list.

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4. A computer-implemented method as recited in claim 1 wherein a first set of methods selected from the plurality of methods is associated with a first list and a second set of methods selected from the plurality of methods is associated with a second list, the first list being created during the processing of the computer program the second list being created before the processing of the computer program, wherein identifying the first method includes:

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selecting the first method as a highest priority method from the second list.

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5. A computer-implemented method as recited in any

one of the preceding claims further including:

determining (308) whether an interrupt is received by the computer program after the initialization of the compilation of the first method; continuing (314) the compilation of the first method for a predetermined period of time when it is determined that an interrupt has been received;
 determining (316) whether the compilation of the first method is completed after the predetermined period of time; and
 aborting (318) the compilation of the first method when it is determined that the compilation of the first method is not completed after the pre-determined period of time.

6. A computer-implemented method as claimed in claim 1 wherein the idle period is a period of low activity in the overall processing of a computer program.

7. A computer-implemented method as claimed in claim 1, further comprising determining (304) whether the first method has already been compiled, wherein the determination is made during the idle period.

8. A computer-implemented method as recited in claim 7 further including:

identifying a second interpretable method selected from the plurality of interpretable methods during the idle period when it is determined that the first method has already been compiled; determining whether the second method has already been compiled, wherein the determination of whether the second method has already been compiled occurs during the idle period; and initializing a compilation of the second method during the idle period when it is determined that the second method has not already been compiled.

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9. A computer-implemented method as recited in claim 1 further including:

determining a priority value for each of the plurality of methods, wherein the first method is identified based on the priority value for the first method.

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10. A computer-implemented method as recited in claim 9 wherein determining the priority value for each of the plurality of methods includes processing an invocation counter associated with each of the plurality of methods during the period of low activity.

11. A computer-implemented method as recited in claim 9 wherein determining the priority value for each of the plurality of methods includes estimating a compilation time for each of the plurality of methods during the period of low activity. 5

12. A computer-implemented method as recited in claim 5 further including:

completing (312) the compilation of the first method when it is determined (308) that an interrupt has not been received; 10

identifying a second interpretable method selected from the plurality of interpretable methods during the period of low activity;

determining whether the second method has already been compiled during the period of low activity; and

initializing a compilation of the second method during the period of low activity when it is determined that the second method has not already been compiled. 15

13. A computer-implemented method as recited in claim 1 further including completing the compilation of the first method when an interrupt is received. 20

14. A computer-implemented method as recited in claim 1 further including suspending the compilation of the first method when an interrupt is received. 25

15. A computer system for dynamically compiling methods during an idle period in the processing of a computer program, the computer system comprising:

a mechanism for identifying the idle period; 30

a mechanism for identifying a first interpretable method selected from a plurality of interpretable methods during the idle period, the plurality of methods being included in the computer program and the computer program being executable using both interpreted program code and compiled program code; and

a compiler for initializing a compilation of the first method during the idle period. 35

16. A computer system as recited in claim 15 further including:

a first list arranged to reference a first set of the plurality of methods, wherein the first list is associated with the computer program and the first method is a highest priority method associated with the first list; and 40

a second list arranged to reference a second set of the plurality of methods, wherein the second list is created before the processing of the computer program and the first list is created during

the processing of the computer program. 45

17. A computer system as recited in claim 15 further including:

an interrupt processor arranged to receive an interrupt signal during the compilation of the first method, the interrupt processor being in communication with the compiler, wherein the compiler is further arranged to continue the compilation of the first method for a predetermined period of time in response to a signal from the interrupt processor; and

an aborting mechanism, the aborting mechanism being in communication with the compiler, wherein the aborting mechanism is arranged to abort the compilation of the first method. 50

18. A computer system as recited in any one of claims 15 to 17 further including:

a diagnostic mechanism, the diagnostic mechanism being arranged to identify when the first method is compiled. 55

19. Computer program code executable by a data processing device to provide the method of any of claims 1 to 14 or the computer system of any of claims 15 to 18. 60

20. A computer program product comprising a computer readable medium bearing computer program code as claimed in claim 19. 65

35 Revendications

1. Procédé implémenté par ordinateur pour compiler dynamiquement des procédés durant une période d'inactivité dans le traitement d'un programme informatique, le procédé implémenté par ordinateur comportant des étapes consistant à :

identifier (212) la période d'inactivité ; 45

identifier (302) un premier procédé interprétable sélectionné parmi une pluralité de procédés interprétables durant la période d'inactivité, la pluralité de procédés étant inclus dans le programme informatique, le programme informatique (144) étant exécutable en utilisant à la fois un code de programme interprétré (154) et un code de programme compilé (152) ; et

initialiser (306) une compilation du premier procédé, dans lequel l'initialisation de la compilation du premier procédé se produit durant la période d'inactivité. 50

2. Procédé implémenté par ordinateur selon la revendication 1, caractérisé par ce que :

le procédé implémenté par ordinateur comprend une liste de compilation initialisée durant la période d'inactivité, la liste de compilation initialisée étant associée au programme informatique et le premier procédé interprétable étant la méthode de la liste de compilation initialisée à laquelle la priorité est la plus élevée ; et

la liste de compilation initialisée est créée avant le traitement du programme informatique, la liste de compilation initialisée étant créée durant la période d'inactivité. 55

dication 1, dans lequel la pluralité de procédés est référencée dans une première liste associée au programme informatique et le premier procédé est un procédé à la plus haute priorité associé à la première liste.

3. Procédé implémenté par ordinateur selon la revendication 1, dans lequel un premier ensemble de procédés sélectionnés parmi la pluralité de procédés est associé à une première liste et un second ensemble de procédés sélectionnés parmi la pluralité de procédés est associé à une seconde liste, la première liste étant créée durant le traitement du programme informatique, la seconde liste étant créée avant le traitement du programme informatique, dans lequel l'identification du premier procédé inclut :

la sélection du premier procédé en tant que procédé à la plus haute priorité dans la première liste.

4. Procédé implémenté par ordinateur selon la revendication 1, dans lequel un premier ensemble de procédés sélectionnés parmi la pluralité de procédés est associé à une première liste et un second ensemble de procédés sélectionnés parmi la pluralité de procédés est associé à une seconde liste, la première liste étant créée durant le traitement du programme informatique, la seconde liste étant créée avant le traitement du programme informatique, dans lequel l'identification du premier procédé inclut :

la sélection du premier procédé en tant que procédé à la plus haute priorité dans la seconde liste.

5. Procédé implémenté par ordinateur selon l'une quelconque des revendications précédentes, incluant de plus les étapes consistant à :

déterminer (308) si une interruption est reçue par le programme informatique après l'initialisation de la compilation du premier procédé ; continuer (314) la compilation du premier procédé pendant une période de temps prédéterminée lorsqu'il est déterminé qu'une interruption a été reçue ; déterminer (616) si la compilation du premier procédé est achevée après la période de temps prédéterminée ; et abandonner (318) la compilation du premier procédé lorsqu'il est déterminé que la compilation du premier procédé n'est pas achevée après la période de temps prédéterminée.

6. Procédé implémenté par ordinateur selon la revendication 1, dans lequel la période d'inaktivité est une période de faible activité dans le traitement total d'un programme informatique.

5 7. Procédé implémenté par ordinateur selon la revendication 1, comportant de plus le fait de déterminer (304) si le premier procédé a déjà été compilé, dans lequel la détermination est effectuée durant la période d'inaktivité.

10 8. Procédé implémenté par ordinateur selon la revendication 7, incluant de plus les étapes consistant à :

identifier un second procédé interprétable sélectionné parmi la pluralité de procédés interprétables durant la période d'inaktivité lorsqu'il est déterminé que le premier procédé a déjà été compilé ; déterminer si le second procédé a déjà été compilé, dans lequel la détermination indiquant si le second procédé a déjà été compilé se produit durant la période d'inaktivité ; et initialiser une compilation du second procédé durant la période d'inaktivité lorsqu'il est déterminé que le second procédé n'a pas été déjà compilé.

15 9. Procédé implémenté par ordinateur selon la revendication 1, incluant de plus :

la détermination d'une valeur de priorité pour chacun de la pluralité de procédés, dans lequel le premier procédé est identifié sur la base de la valeur de priorité du premier procédé.

20 10. Procédé implémenté par ordinateur selon la revendication 9, dans lequel la détermination de la valeur de priorité pour chacun de la pluralité de procédés inclut le traitement d'un compteur d'appels associé à chacun de la pluralité de procédés durant la période de faible activité.

25 11. Procédé implémenté par ordinateur selon la revendication 9, dans lequel la détermination de la valeur de priorité pour chacun de la pluralité de procédés inclut l'estimation d'un temps de compilation pour chacun de la pluralité de procédés durant la période de faible activité.

30 12. Procédé implémenté par ordinateur selon la revendication 5, incluant de plus les étapes consistant à :

achever (312) la compilation du premier procédé lorsqu'il est déterminé (308) qu'une interruption n'a pas été reçue ; identifier un second procédé interprétable sélectionné parmi la pluralité de procédés interprétables durant la période de faible activité ;

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déterminer si le second procédé a déjà été compilé durant la période de faible activité ; et initialiser une compilation du second procédé durant la période de faible activité lorsqu'il est déterminé que le second procédé n'a pas été déjà compilé. 5

13. Procédé implémenté par ordinateur selon la revendication 1, incluant de plus l'achèvement de la compilation du premier procédé lorsqu'une interruption est reçue. 10

14. Procédé implémenté par ordinateur selon la revendication 1, incluant de plus la suspension de la compilation du premier procédé lorsqu'une interruption est reçue. 15

15. Système informatique pour compiler dynamiquement des procédés durant une période d'inactivité dans le traitement d'un programme informatique, le système informatique comportant :
 un mécanisme pour identifier la période d'inactivité ;
 un mécanisme pour identifier un premier procédé interprétable sélectionné parmi une pluralité de procédés interprétables durant la période d'inactivité, la pluralité de procédés étant inclus dans le programme informatique et le programme informatique étant exécutable en utilisant à la fois un code de programme interpréte et un code de programme compilé ; et
 un compilateur pour initialiser une compilation du premier procédé durant la période d'inactivité. 25

16. Système informatique selon la revendication 15, incluant de plus :
 une première liste agencée pour référencer un premier ensemble de la pluralité de procédés, dans lequel la première liste est associée au programme informatique et le premier procédé est un procédé à la plus haute priorité associé à la première liste ;
 une seconde liste conçue pour référencer un second ensemble de la pluralité de procédés, dans lequel la seconde liste est créée avant le traitement du programme informatique et la première liste est créée durant le traitement du programme informatique. 30

17. Système informatique selon la revendication 15, incluant de plus :
 un processeur d'interruption agencé pour recevoir un signal d'interruption durant la compilation du premier procédé, le processeur d'interruption étant en communication avec le compilateur, dans lequel le compilateur est de plus agencé pour continuer la compilation du premier procédé pendant une période de temps prédéterminée en réponse à un signal provenant du processeur d'interruption ; et un mécanisme d'abandon, le mécanisme d'abandon étant en communication avec le compilateur, dans lequel le mécanisme d'abandon est agencé pour abandonner la compilation du premier procédé. 35

18. Système informatique selon l'une quelconque des revendications 15 à 17, incluant de plus :
 un mécanisme de diagnostic, le mécanisme de diagnostic étant agencé pour identifier quand le premier procédé est compilé. 40

19. Code de programme informatique exécutable par un dispositif de traitement de données pour fournir le procédé selon l'une quelconque des revendications 1 à 14 ou le système informatique selon l'un quelconque des revendications 15 à 18. 45

20. Produit de programme informatique comportant un support lisible par ordinateur contenant un code de programme informatique selon la revendication 19. 50

Patentansprüche

1. Per Computer implementiertes Verfahren zum dynamischen Kompilieren von Verfahren während eines Leerlaufzeitraums in der Verarbeitung eines Computerprogramms, wobei das per Computer implementierte Verfahren Folgendes umfasst :
 Identifizieren (212) des Leerlaufzeitraums; Identifizieren (302) eines ersten interpretierbaren Verfahrens, das während des Leerlaufzeitraums aus mehreren interpretierbaren Verfahren ausgewählt wurde, wobei die mehreren Verfahren in dem Computerprogramm enthalten sind, wobei das Computerprogramm (144) unter Verwendung von sowohl interpretiertem Programmcode (154) als auch kompiliertem Programmcode (152) ausgeführt werden kann; und Initialisieren (306) einer Kompilierung des ersten Verfahrens, wobei die Initialisierung der Kompilierung des ersten Verfahrens während des Leerlaufzeitraums erfolgt. 55

2. Per Computer implementiertes Verfahren nach Anspruch 1, wobei auf die mehreren Verfahren in einer ersten Liste, die dem Computerprogramm zugeordnet ist, verwiesen wird und es sich bei dem ersten Verfahren um ein Verfahren von oberster Priorität,

das der ersten Liste zugeordnet ist, handelt.

3. Per Computer implementiertes Verfahren nach Anspruch 1, wobei eine erste Gruppe von Verfahren, die aus den mehreren Verfahren ausgewählt wurde, einer ersten Liste zugeordnet ist und eine zweite Gruppe von Verfahren, die aus den mehreren Verfahren ausgewählt wurde, einer zweiten Liste zugeordnet ist, wobei die erste Liste während der Verarbeitung des Computerprogramms erstellt wird und die zweite Liste vor der Verarbeitung des Computerprogramms erstellt wird, wobei das Identifizieren des ersten Verfahrens beinhaltet:

Auswählen des ersten Verfahrens als ein Verfahren von oberster Priorität aus der ersten Liste.

4. Per Computer implementiertes Verfahren nach Anspruch 1, wobei eine erste Gruppe von Verfahren, die aus den mehreren Verfahren ausgewählt wurde, einer ersten Liste zugeordnet ist und eine zweite Gruppe von Verfahren, die aus den mehreren Verfahren ausgewählt wurde, einer zweiten Liste zugeordnet ist, wobei die erste Liste während der Verarbeitung des Computerprogramms erstellt wird und die zweite Liste vor der Verarbeitung des Computerprogramms erstellt wird, wobei das Identifizieren des ersten Verfahrens beinhaltet:

Auswählen des ersten Verfahrens als ein Verfahren von oberster Priorität aus der zweiten Liste.

5. Per Computer implementiertes Verfahren nach einem der vorangehenden Ansprüche, das des Weiteren Folgendes enthält:

Feststellen (308), ob ein Interrupt durch das Computerprogramm nach der Initialisierung der Kompilierung des ersten Verfahrens empfangen wird; Fortsetzen (314) der Kompilierung des ersten Verfahrens für die Dauer eines vorgegebenen Zeitraums, wenn festgestellt wird, dass ein Interrupt empfangen wurde;
Feststellen (316), ob die Kompilierung des ersten Verfahrens nach dem vorgegebenen Zeitraum abgeschlossen ist; und
Abbrechen (318) der Kompilierung des ersten Verfahrens, wenn festgestellt wird, dass die Kompilierung des ersten Verfahrens nach dem vorgegebenen Zeitraum nicht abgeschlossen ist.

6. Per Computer implementiertes Verfahren nach Anspruch 1, wobei der Leerlaufzeitraum ein Zeitraum geringer Aktivität innerhalb der Gesamtverarbeitung eines Computerprogramms ist.

7. Per Computer implementiertes Verfahren nach Anspruch 1, das des Weiteren das Feststellen (304) umfasst, ob das erste Verfahren bereits kompiliert wurde, wobei die Feststellung während des Leerlaufzeitraums getroffen wird.

8. Per Computer implementiertes Verfahren nach Anspruch 7, das des Weiteren Folgendes beinhaltet:

Identifizieren eines zweiten interpretierbaren Verfahrens, das während des Leerlaufzeitraums aus den mehreren interpretierbaren Verfahren ausgewählt wurde, wenn festgestellt wird, dass das erste Verfahren bereits kompiliert wurde;
Feststellen, ob das zweite Verfahren bereits kompiliert wurde, wobei die Feststellung, ob das zweite Verfahren bereits kompiliert wurde, während des Leerlaufzeitraums getroffen wird; und Initialisieren einer Kompilierung des zweiten Verfahrens während des Leerlaufzeitraums, wenn festgestellt wird, dass das zweite Verfahren noch nicht kompiliert wurde.

9. Per Computer implementiertes Verfahren nach Anspruch 1, das des Weiteren Folgendes beinhaltet:

Ermitteln eines Prioritätswertes für jedes der mehreren Verfahren, wobei das erste Verfahren anhand des Prioritätswertes für das erste Verfahren identifiziert wird.

10. Per Computer implementiertes Verfahren nach Anspruch 9, wobei das Ermitteln des Prioritätswertes für jedes der mehreren Verfahren das Verarbeiten eines Aufrufzählers, der jedem der mehreren Verfahren zugeordnet ist, während des Zeitraums geringer Aktivität beinhaltet.

11. Per Computer implementiertes Verfahren nach Anspruch 9, wobei das Ermitteln des Prioritätswertes für jedes der mehreren Verfahren das Schätzen einer Kompilierungsdauer für jedes der mehreren Verfahren während des Zeitraums geringer Aktivität beinhaltet.

12. Per Computer implementiertes Verfahren nach Anspruch 5, das des Weiteren Folgendes beinhaltet:

Zuendführen (312) der Kompilierung des ersten Verfahrens, wenn festgestellt wird (308), dass kein Interrupt empfangen wurde;
Identifizieren eines zweiten interpretierbaren Verfahrens, das während des Zeitraums geringer Aktivität aus den mehreren interpretierbaren Verfahren ausgewählt wurde;
Feststellen, ob das zweite Verfahren bereits während des Zeitraums geringer Aktivität kom-

piliert wurde; und Initialisieren einer Kompilierung des zweiten Verfahrens während des Zeitraums geringer Aktivität, wenn festgestellt wird, dass das zweite Verfahren noch nicht kompiliert wurde.	5	rung des ersten Verfahrens zu erhalten, wobei der Interruptprozessor mit dem Kompilierer im Datenaustausch steht, wobei der Kompilierer des Weiteren dafür konfiguriert ist, in Reaktion auf ein Signal von dem Interruptprozessor die Kompilierung des ersten Verfahrens für eine vorgegebene Zeitdauer fortzuführen; und einen Abbrechmechanismus, wobei der Abbrechmechanismus mit dem Kompilierer im Datenaustausch steht, wobei der Abbrechmechanismus dafür konfiguriert ist, die Kompilierung des ersten Verfahrens abzubrechen.
13. Per Computer implementiertes Verfahren nach Anspruch 1, das des Weiteren das Zuendeführen der Kompilierung des ersten Verfahrens beinhaltet, wenn ein Interrupt empfangen wird.	10	18. Computersystem nach einem der Ansprüche 15 bis 17, das des Weiteren Folgendes enthält:
14. Per Computer implementiertes Verfahren nach Anspruch 1, das des Weiteren das Aussetzen der Kompilierung des ersten Verfahrens beinhaltet, wenn ein Interrupt empfangen wird.	15	einen Diagnosemechanismus, wobei der Diagnosemechanismus dafür konfiguriert ist festzustellen, wann das erste Verfahren kompiliert ist.
15. Computersystem zum dynamischen Kompilieren von Verfahren während eines Leerlaufzeitraums in der Verarbeitung eines Computerprogramms, wobei das Computersystem Folgendes umfasst:	20	19. Computerprogrammcode, der durch eine Datenverarbeitungsvorrichtung ausgeführt werden kann, um das Verfahren nach einem der Ansprüche 1 bis 14 bereitzustellen, oder der durch das Computersystem nach einem der Ansprüche 15 bis 18 ausgeführt werden kann.
einen Mechanismus zum Identifizieren des Leerlaufzeitraums; einen Mechanismus zum Identifizieren eines ersten interpretierbaren Verfahrens, das während des Leerlaufzeitraums aus mehreren interpretierbaren Verfahren ausgewählt wurde, wobei die mehreren Verfahren in dem Computerprogramm enthalten sind und das Computerprogramm unter Verwendung von sowohl interpretiertem Programmcode als auch kompiliertem Programmcode ausgeführt werden kann; und einen Kompilierer zum Initialisieren einer Kompilierung des ersten Verfahrens während des Leerlaufzeitraums.	25	20. Computerprogrammprodukt, umfassend ein computerlesbares Speichermedium, auf dem ein Computerprogrammcode nach Anspruch 19 gespeichert ist.
16. Computersystem nach Anspruch 15, das des Weiteren Folgendes enthält:	30	
eine erste Liste, die dafür konfiguriert ist, auf eine erste Gruppe der mehreren Verfahren zu verweisen, wobei die erste Liste dem Computerprogramm zugeordnet ist und es sich bei dem ersten Verfahren um ein Verfahren von oberster Priorität, das der ersten Liste zugeordnet ist, handelt; und	40	
eine zweite Liste, die dafür konfiguriert ist, auf eine zweite Gruppe der mehreren Verfahren zu verweisen, wobei die zweite Liste vor der Verarbeitung des Computerprogramms erstellt wird und die erste Liste während der Verarbeitung des Computerprogramms erstellt wird.	45	
17. Computersystem nach Anspruch 15, das des Weiteren Folgendes enthält:	50	
einen Interruptprozessor, der dafür konfiguriert ist, ein Interruptsignal während der Kompilie-	55	

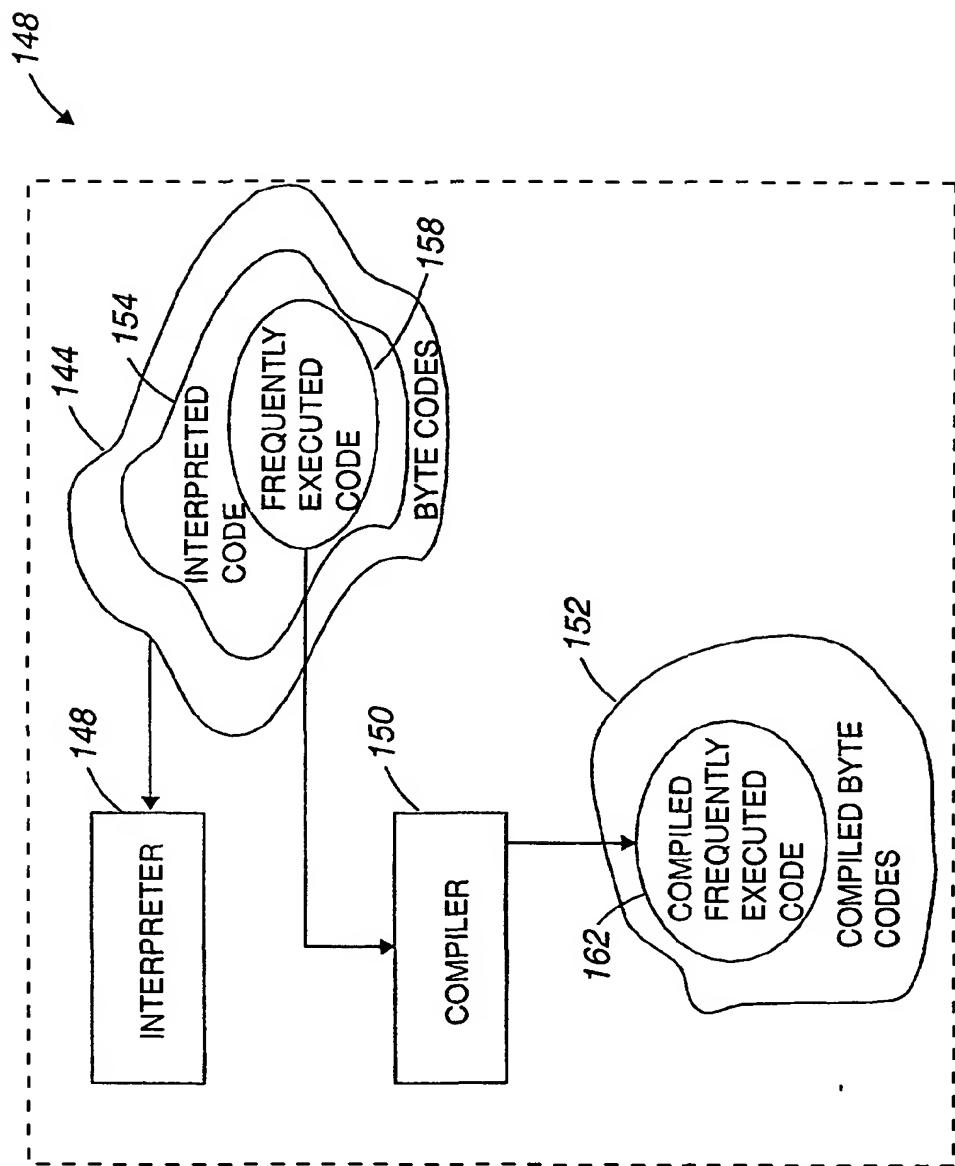


Figure 1

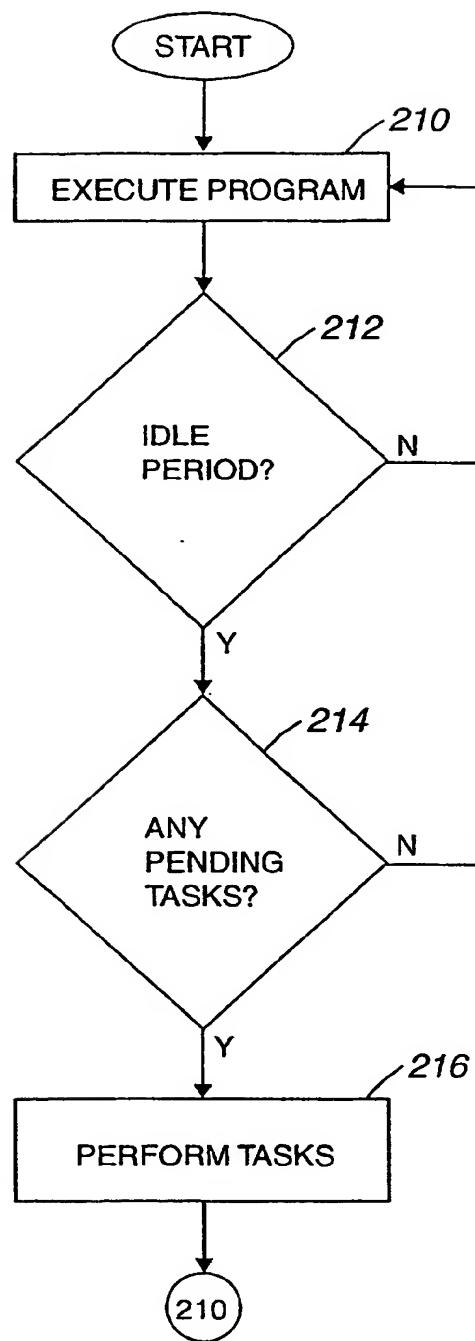


Figure 2

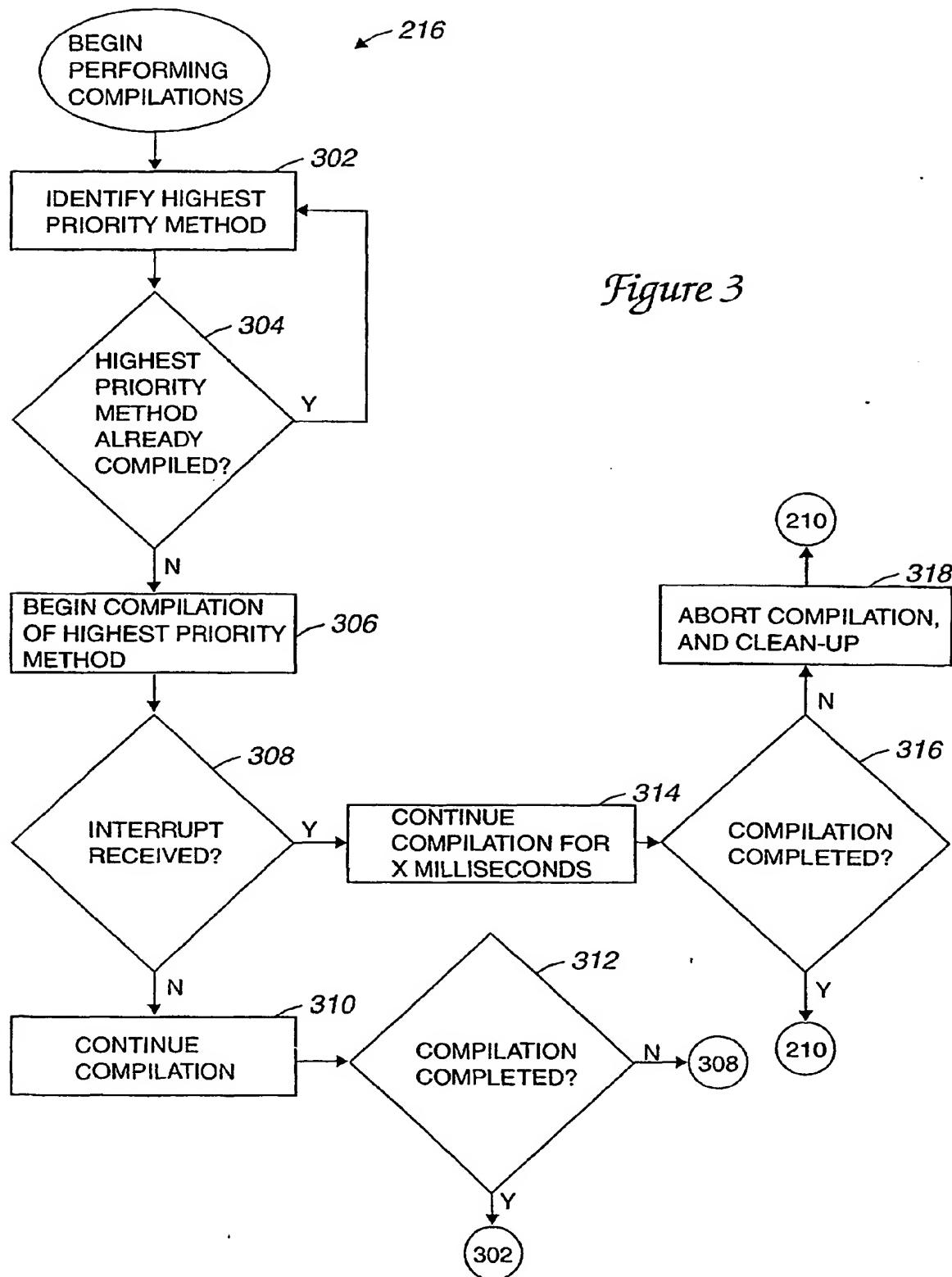


Figure 3

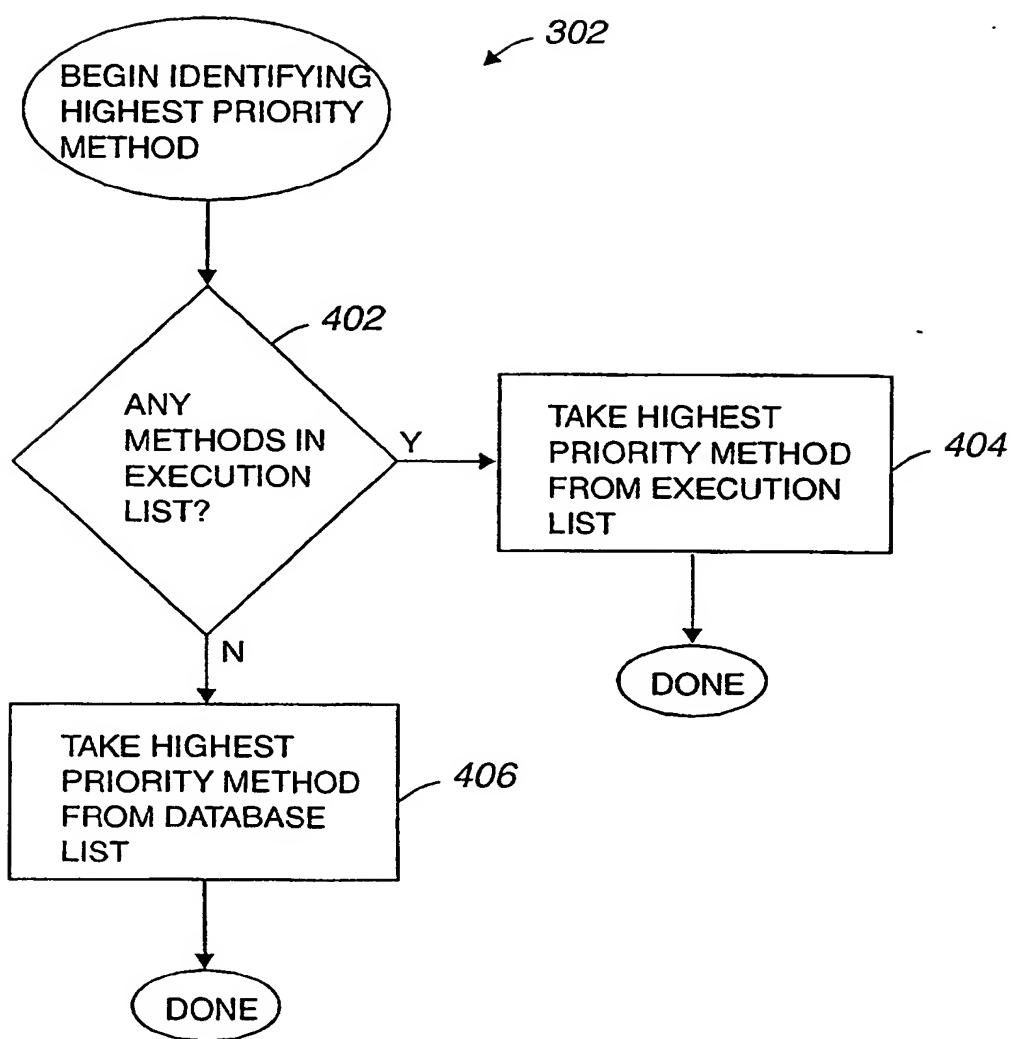


Figure 4

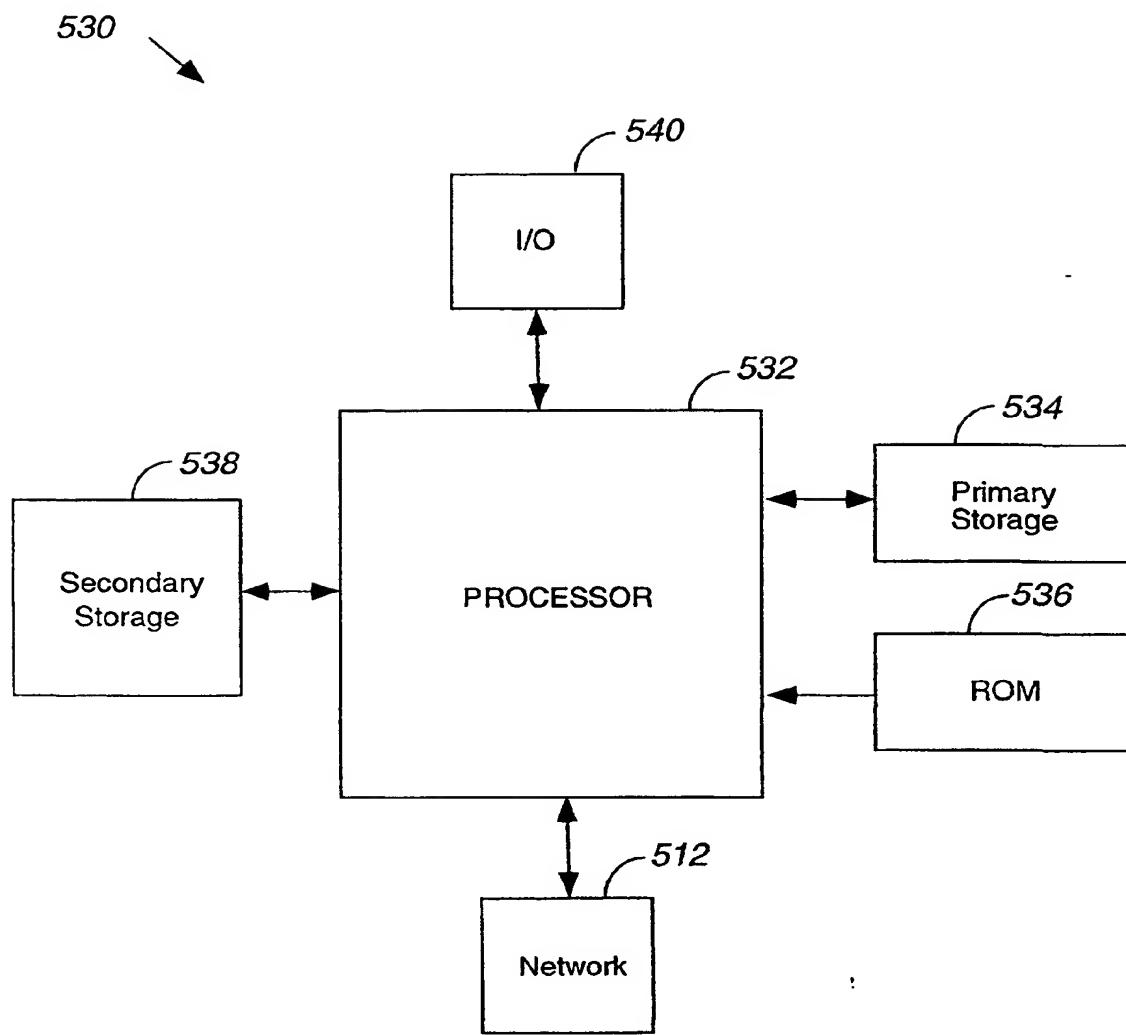


Figure 5

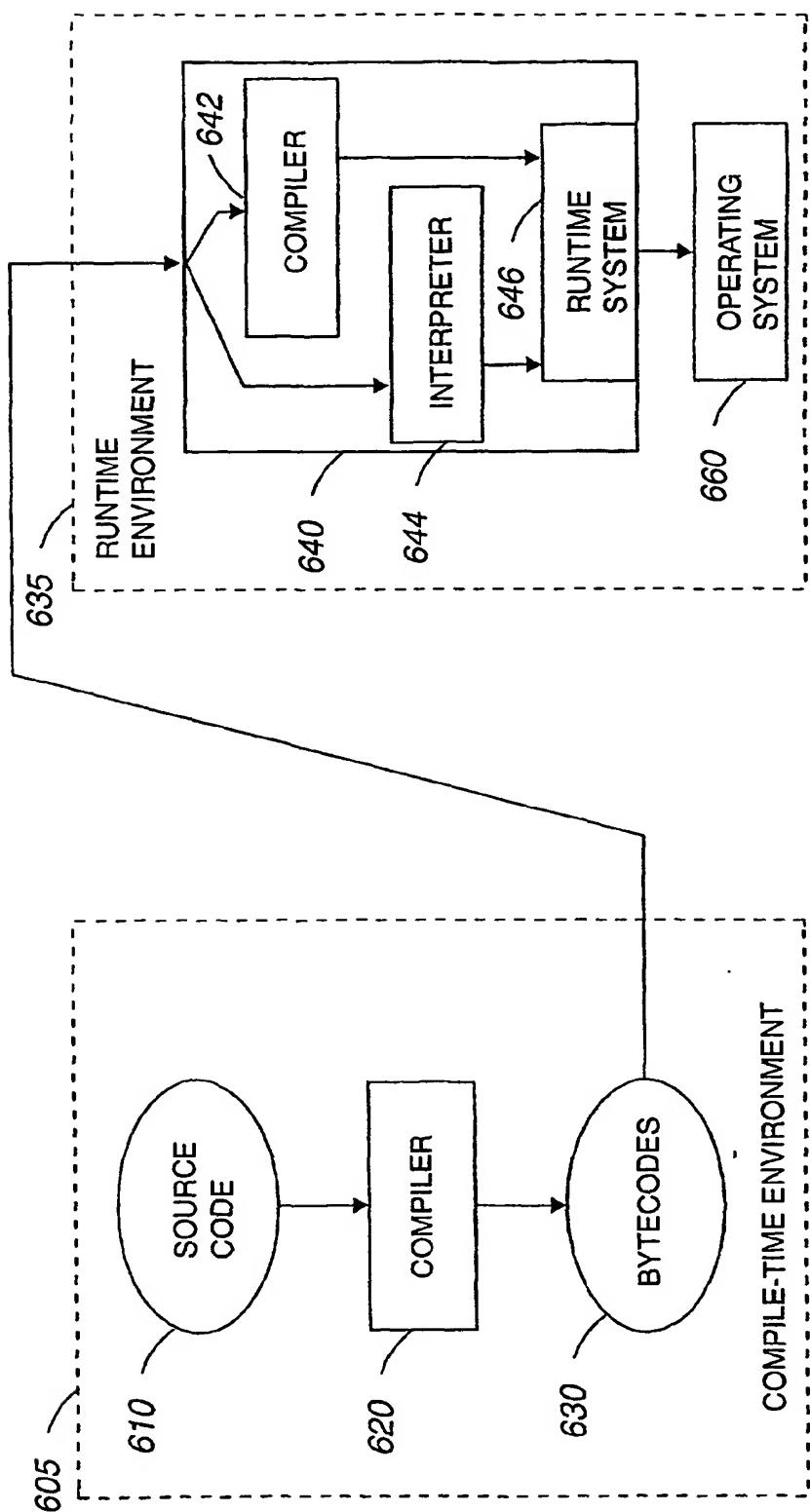


Figure 6